



Espacenet

Bibliographic data: JP 2001103734 (A)

SWITCHING CONVERTER

Publication date: 2001-04-13
Inventor(s): NAGAHARA KIYOKAZU +
Applicant(s): SONY CORP +
Classification:
 - **international:** *H02M1/00; H02M1/32; H02M1/36; H02M3/155; H02M3/22; H02M3/28;* (IPC1-7): H02M3/155
 - **europaen:** H02M1/32; H02M1/36
Application number: JP19990275195 19990928
Priority number (s): JP19990275195 19990928
Also published as:
 • EP 1089421 (A2)
 • EP 1089421 (A3)
 • US 6343025 (B1)
 • KR 20010050574 (A)
 • KR 100673741 (B1)
 • more

Abstract of JP 2001103734 (A)

PROBLEM TO BE SOLVED: To prevent an erroneous operation of a non-control detection protective circuit when starting a switching converter. **SOLUTION:** A latch controller 40 invalidates a non-control detection decision signal SDT when softly started. A drive circuit 33 stops drive signals SD1, SD2 for a predetermined period after an overload state is detected based on the signal SDT, then controls a soft start controller 35 to softly start, thereby executing an intermittent operation. Further, an intermittent controller 50 controls the controller 35, thereby completing discharging of a capacitor 36 for setting the soft starting period when started at the soft starting operation in the intermittent operation.

Last updated:
 26.04.2011 Worldwide
 Database 5.7.22: 93p

第1の実施の形態における回路構成例

